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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) IND10320
APPLN. NO.: 09/532,807
FILED: 03/21/00
TITLE: CIRCUIT CHIP CONNECTOR AND METHOD OF CONNECTING A CIRCUIT CHIP

GROUP ART UNIT: 3729
EXAMINER: Arbes, Carl J.

4
Election
C. J. Arbes
5/17/02

Certificate of Mailing

Date of deposit: April 23, 2002

I hereby certify that this paper is being deposited with the United States Postal Service on the date indicated above, as first-class mail, with sufficient postage attached thereto, in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C.

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Sheila Mannerino

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RESPONSE TO RESTRICTION REQUIREMENT

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MAY 08 2002

Assistant Commissioner for Patents
Washington, D.C. 20231

TECHNOLOGY CENTER R3700

Sir:

This paper is in response to the restriction requirement in the Office Action mailed April 9, 2002.

REMARKS

In response to the restriction requirement, Applicant provisionally elects Group I (claims 1-12), without traverse, if applicable.

Reconsideration of this application is respectfully requested.

Respectfully submitted,

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